

October 12, 2023

To All Chairpersons of Concerned Institutions

OGAWA Hisao  
Dean, Research and Education Institute for Semiconductors and Informatics  
Kumamoto University

Recruitment of Professor or Associate Professor at the Research and Education Institute for Semiconductors and Informatics, Kumamoto University

We are pleased to announce that Kumamoto University is now inviting applications for Professor and Associate Professor positions at the Research and Education Institute for Semiconductors and Informatics.

If possible, we would appreciate your recommendation of qualified candidates from your institution. The recruitment details were as follows:

#### Details

1. Name and Number of Positions: Professor or Associate Professor, two positions
2. Starting Date of Appointment: Earliest possible date on or after April 1<sup>st</sup>, 2024
3. Affiliations: Department of Forefront Semiconductor, Division of Semiconductors, Research and Education Institute for Semiconductors and Informatics, Kumamoto University.
4. Field of Research:  
Next-generation semiconductor materials and/or semiconductor device simulation
5. Subjects in charge: Undergraduate and graduate courses on the basics and applications of semiconductor device engineering.
6. Application deadline: Applications must be received no later than November 30<sup>th</sup>, 2023
7. Candidate Qualifications:
  - (1) PhD holders
  - (2) The candidate must have sufficient ability and enthusiasm for education and research in either or both of the following research areas, and be active in international and social educational and research activities:
    - a) Next-generation semiconductor materials, including power semiconductors that realize high frequency, high withstand voltage, and low power loss.
    - b) Semiconductor device and material simulations related to the structure of advanced semiconductors such as GAA, nanosheets, and nanowires
  - (3) Those who can actively engage in industry-academia-government collaborations, collaborate with the local community, and contribute to the creation of new semiconductor (and related) businesses.
  - (4) Those who have sufficient ability and enthusiasm for university management and operations.
  - (5) Those with a track record of acquiring external funds, and who can continue to acquire research funds in the future (However, a record of external fund acquisitions is not essential for those who belong private companies.)
  - (6) Those who can communicate and provide research guidance and lectures in English.

(7) Non-Japanese speakers should have Japanese proficiency to carry out various duties inside and outside the university.

8. Working conditions:

- (1) Work details: Discretionary labor systems for professional work  
Working hours will be left to the discretion of the employee, with working hours per day deemed to be 7 hours and 45 minutes.
- (2) Terms of employment: Not fixed
- (3) Probationary period: Six months
- (4) Work Location: Kurokami Campus, Kumamoto University
- (5) Overtime work: Overtime, late-night, or holiday work may be required.
- (6) Wages, etc.: As set forth in the Kumamoto University Regulations on Salary for Employees Subject to the Annual Salary System (2<sup>nd</sup> category)
- (7) Social insurance: Health insurance from the Mutual Aid Association of MEXT, employment insurance, and workers' compensation insurance
- (8) Employer: Kumamoto University.

9. Required documents for applications

- (1) Curriculum vitae (CV)/resume (any form/format is acceptable) 1  
\* Includes applicant photographs, current physical address, and email address.  
\* Specify any period when you were on special leave, including but not limited to maternity, child, family, or nursing care, which you wish to be considered during the candidate review.
- (2) Research biography and achievement list 1  
Please fill in and submit the template summary table below. Additionally, please submit an achievement list (arbitrary form), which will be the basis of this.  
[https://www.cs.kumamoto-u.ac.jp/~iida/REISI/koubo\\_a\\_gyoseki.docx](https://www.cs.kumamoto-u.ac.jp/~iida/REISI/koubo_a_gyoseki.docx)
- (3) Prints or copies of representative papers (10 max) 1
- (4) Aspiration for education (about 1,000 words on A4 size paper) 1
- (5) Aspiration for research (about 1,000 words on A4 size paper) 1
- (6) List of references 1  
Names, affiliations, and contact information including email addresses and telephone numbers of two people who can act as professional references.

10. Submission of Documents:

All required documents and a CD-ROM or USB memory containing the PDF file should be sent by mail to the address below. Please write "Application documents for Faculty members of the Research and Education Institute for Semiconductors and Informatics" in red on the front cover of the registered mail envelope/parcel.

11. Screening Process:

Preliminary Screening (Document Review): Early December, 2023

Secondary Screening (Interview): Late December, 2023

\*Any costs related to the interview, such as transportation and accommodation expenses, will not be reimbursed by Kumamoto University. Online interviews are also accepted. Please contact us after submitting the documents for details.

12. Document Submission Address:

Professor Masahiro Iida

Division of Semiconductors, Research and Education Institute for Semiconductors and Informatics,  
Kumamoto University  
2-39-1 Kurokami, Chuo-ku, Kumamoto  
860-8555, Japan  
E-mail: iida@cs.kumamoto-u.ac.jp  
\* Application documents will not be returned.

13. Contact Information:

Professor Masahiro Iida  
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14. Remarks

- (1) Personal information in the application documents will only be used for personal selection and not for any other purpose, in accordance with the regulations of the National University Corporation, Kumamoto University. Application documents and submitted materials will not be returned.
- (2) Kumamoto University also promotes diversity.
- (3) When evaluating performance, it will be ensured that applicants are not at a disadvantage with respect to maternity leave, childcare/nursing care leave, or short working hours for childcare/nursing care.
- (4) Based on the provisions of Article 8 of the Act on Ensuring Equal Opportunities and Treatment for Men and Women in the Field of Employment, as a measure to improve the enrollment rate of female teachers, we will preferentially hire women if the required abilities are recognized as equivalent.
- (5) Please refer to the following URL for child-rearing support and gender equality initiatives at Kumamoto University: [http://diversity.kumamoto-.ac.jp/activities/kihon\\_keikaku.html](http://diversity.kumamoto-.ac.jp/activities/kihon_keikaku.html)